

## AMENDMENTS

### In the Specification

Please replace the paragraph numbered (10) on Page 3, with the following amended paragraph:

(10) U.S. Patent No. 4,939,516 issued ~~June~~ July 3, 1990 and entitled "Chopper Stabilized Delta-Sigma Analog-to-Digital Converter", *Early* et al inventors (hereinafter, "*Early*"), and U.S. Patent No. 5,039,989 issued August 13, 1991 and entitled "Delta-Sigma Analog-to-Digital Converter with Chopper Stabilization at the Sampling Frequency," *Welland* et al inventors (hereinafter, "*Welland*"), describe conventional applications of chopping in analog-to-digital converters (hereinafter "ADC"). *Early* and *Welland* proposed solutions using chopping circuitry to address 1/f and other noise issues that are particularly unique to ADCs.